

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	9	((I/O (input/output)) adj2 pad) with (power adj2 supply)) with isolat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:27
L2	6	((cell\$4 adj phone) pda (laptop notebook)) and (cpu processor (micro adj processor)) and (power adj supply)) and ((isolat\$3 adj2 circuit) with (pmos pfet p-channel p-mos p-fet))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:27
L3	41	((zero near3 power near4 standby) and (pmos p-mos p-channel pfet)) and ((cpu processor) and (power adj2 supply))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:27
L4	24	((zero near3 power near4 standby) and (pmos p-mos p-channel pfet)) and (processor and (power adj2 supply))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:27
L5	43	((zero near3 power near4 standby) and (pmos p-mos p-channel pfet)) and leak\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:28
L6	10	(standby near3 (mode operation)) same ((isolat\$3 near3 (transistor fet mosfet \$1fet)) with (p-channel pfet p-fet p-mos pmos))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:28
L7	67	(standby near3 (mode operation)) and ((isolat\$3 near3 (transistor fet mosfet \$1fet)) with (p-channel pfet p-fet p-mos pmos))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:28
L8	24	((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:28
L9	12	(((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:28

L10	8	((((processor CPU microprocessor) and ((power adj2 supply) with bus) and switch) and memory) and (power adj2 signal)) and (isolation adj circuit)) and ((MOS or mosfet or fet or transistor) with switch)) and pad	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:28
L11	135	(((input output) adj2 pad buffer) and ((internal near3 bus) with (power adj2 supply))) and control) and (transistor FET MOSFET)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:29
L12	19	(((power adj supply adj bus) and (isolat\$3 with (power adj supply))) and ((cpu processor microprocessor) with (power adj supply))) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:29
L13	16	(US-6400595-\$ or US-6377859-\$ or US-6375344-\$ or US-6128682-\$ or US-4551718-\$ or US-6445222-\$ or US-6396137-\$ or US-5338978-\$ or US-4984204-\$ or US-5924081-\$ or US-5224010-\$ or US-5440519-\$ or US-5563838-\$ or US-5383137-\$ or US-4356550-\$ or US-4276594-\$).did.	USPAT	OR	OFF	2005/02/01 12:29
L14	10	(((power adj2 supply) with disconnect\$3) and (power adj2 supply adj2 bus)) and (((power adj2 supply) with disconnect\$3) and (pda (personaliz\$3 adj2 digital adj2 assistant)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/01 12:29